



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

KRISHNA *et al.*

Appl. No.: 09/610,722

Filed: July 6, 2000

For: **Classification Engine in a
Cryptography Acceleration Chip**

Confirmation No.: 5437

Art Unit: 2136

Examiner: Colin, Carl G.

Atty. Docket: 1875.4310002

Information Disclosure Statement

Mail Stop AF

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Listed on accompanying IDS Forms are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Where the publication date of a listed document does not provide a month of publication, the year of publication of the listed document is sufficiently earlier than the effective U.S. filing date and any foreign priority date so that the month of publication is not in issue. Applicants have listed publication dates on the attached IDS Forms based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may

not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. No statement or fee is required.

Copies of documents FP1-FP3 and NPL1-NPL7 are submitted. However, in accordance with 37 C.F.R. § 1.98(a)(2), no copies of U.S. patents and patent application publications cited on the attached IDS Forms are submitted.

It is respectfully requested that the Examiner initial and return a copy of the enclosed IDS Forms, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

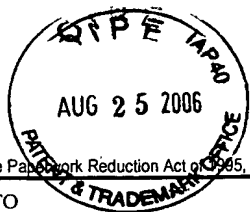
A handwritten signature in black ink, appearing to read "Lori A. Gordon".

Lori A. Gordon
Attorney for Applicants
Registration No. 50,633

Date: August 25, 2006

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	09/610,722
Filing Date	July 6, 2000
First Named Inventor	Suresh KRISHNA
Art Unit	2136
Examiner Name	Colin, Carl G.
Attorney Docket Number	1875.4310002

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If Known)			
	US1	2002/0001384 A1	01-03-2002	Buer <i>et al.</i>	
	US2	2002/0004904 A1	01-10-2002	Blaker <i>et al.</i>	
	US3	2002/0009076 A1	01-24-2002	Engbersen <i>et al.</i>	
	US4	2002/0039418 A1	04-04-2002	Dror <i>et al.</i>	
	US5	2002/0044649 A1	04-18-2002	Gallant <i>et al.</i>	
	US6	2002/0057796 A1	05-16-2002	Lambert <i>et al.</i>	
	US7	2002/0078342 A1	06-20-2002	Matthews, Jr.	
	US8	2002/0097724 A1	07-25-2002	Halme <i>et al.</i>	
	US9	5,161,193	11-03-1992	Lampson <i>et al.</i>	
	US10	5,471,482	11-28-1995	Byers <i>et al.</i>	
	US11	6,115,816	09-05-2000	Davis	
	US12	6,295,604 B1	09-25-2001	Callum	
	US13	6,320,964 B1	11-20-2001	Callum	
	US14	6,349,405 B1	02-19-2002	Welfeld	
	US15	6,378,072 B1	04-23-2002	Collins <i>et al.</i>	
	US16	6,393,026 B1	05-21-2002	Irwin	
	US17	6,421,730 B1	07-16-2002	Narad <i>et al.</i>	
	US18	6,751,677 B1	06-15-2004	Ilnicki <i>et al.</i>	
	US19	6,778,495 B1	08-17-2004	Blair	
	US20	6,831,979 B2	12-14-2004	Callum	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	FP1	EP 0876026 A2	11-04-1998	Greenwood <i>et al.</i>		
	FP2	EP 1132800 A2	09-12-2001	Daspit <i>et al.</i>		
	FP3	WO 02/41599 A1	05-23-2002	Hill <i>et al.</i>		
	FP4					
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

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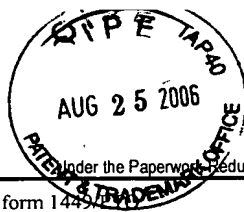
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	09/610,722
				Filing Date	July 6, 2000
				First Named Inventor	Suresh KRISHNA
				Art Unit	2136
				Examiner Name	Colin, Carl G.
Sheet	2	of	2	Attorney Docket Number	1875.4310002

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If Known)			
	US21	6,862,278 B1	03-01-2005	Chang <i>et al.</i>	
	US22	6,996,842 B2	02-07-2006	Strahm <i>et al.</i>	
	US23	7,039,641 B2	05-02-2006	Woo	
	US24	7,055,029 B2	05-30-2006	Collins <i>et al.</i>	
	US25	7,062,657 B2	06-13-2006	Law	
	US26				
	US27				
	US28				
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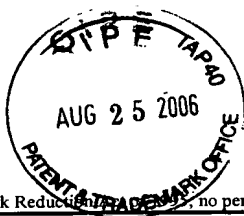
FOREIGN PATENT DOCUMENTS						
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	FP10					
	FP11					
	FP12					
	FP13					
	FP14					
	FP15					
	FP16					
	FP17					
	FP18					

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				Examiner Name	Colin, Carl G.
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published	T ²
	NPL1	Deepakumara, J. <i>et al.</i> , "FPGA Implementation of MD5 Hash Algorithm," <i>Conference Proceedings of the Canadian Conference on Electrical and Computer Engineering</i> , Volume II, pp. 919-924, May 13-16, 2001.	
	NPL2	Burnside, M. and Keromytis, A.D., "Accelerating Application-Level Security Protocols," <i>The 11th IEEE International Conference on Networks</i> , pp. 313-318, September 28 - October 1, 2003.	
	NPL3	Jiang, S. <i>et al.</i> , "Securing Web servers against Insider Attack," <i>Proceedings 17th Annual Computer Security Applications Conference</i> , pp. 265-276, December 10-14, 2001.	
	NPL4	"Server ECS", 3/9/98, [Retrieved from Internet 9/2/04], "http://home.expertcanmore.net/expert/servers.htm".	
	NPL5	IPSec Overview, 6/5/01, [Retrieved from Internet 9/2/04], "http://forsitesolutions.com/Techstuff/freeswan/inside_overview.html".	
	NPL6	"RFC 2402", 10/98, [Retrieved from Internet 9/03/04], "http://www.ietf.org/rfc/rfc2402.txt?number=2402".	
	NPL7	European Search Report, from European Patent Appl. No. 03029195.9, 3 pages, dated November 2, 2005.	
	NPL8		
	NPL9		
	NPL10		

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